

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
4 January 2001 (04.01.2001)

PCT

(10) International Publication Number
WO 01/01453 A2

- (51) International Patent Classification⁷: **H01L**
- (21) International Application Number: PCT/US00/17618
- (22) International Filing Date: 27 June 2000 (27.06.2000)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
09/343,132 29 June 1999 (29.06.1999) US
- (71) Applicant: SUN MICROSYSTEMS, INC. [US/US]; 901 San Antonio Road, MS UPAL1-521, Palo Alto, CA 94303 (US).
- (72) Inventor: NOVAK, Istvan; 81 Apple Ridge Road, Maynard, MA 01754 (US).
- (74) Agent: KUDIRKA, Paul, E.; Kudirka & Jobse, LLP, Two Center Plaza, Boston, MA 02108 (US).

(81) Designated States (*national*): AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, GM, HR, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, UZ, VN, YU, ZW.

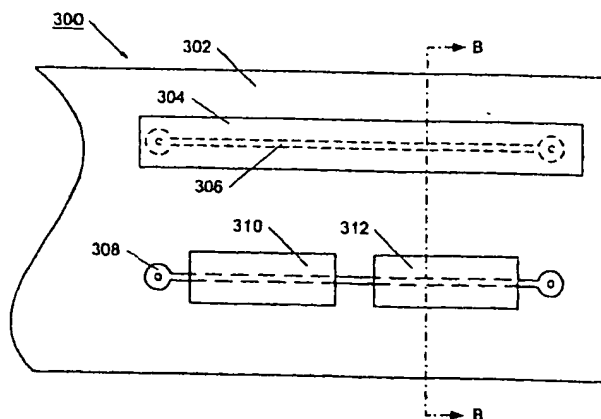
(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:

— Without international search report and to be republished upon receipt of that report.

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: METHOD AND APPARATUS FOR ADJUSTING ELECTRICAL CHARACTERISTICS OF SIGNAL TRACES IN LAYERED CIRCUIT BOARDS



(57) Abstract: The electrical characteristics of a signal trace in a layered circuit board are modified by selectively modifying the dielectric constant and/or the magnetic permeability of an insulating material layer in the vicinity of a signal interconnect. The electrical characteristic is modified by adding a layer of different material into the circuit board layers either above or below the circuit board plane containing the trace. The different material could be any insulating material with a different dielectric and/or permeability constant. In one embodiment, during the circuit board lamination process, only a selected trace is covered with a layer of the different material, thus the electrical properties of other traces will not be affected. The layer of different material on the trace may cover the entire length of the trace, or it covers one or more parts of the trace, as the adjustment of the electrical properties requires. In another embodiment, the insulating material separating the trace from a reference plane is replaced with a different material in the vicinity of the selected trace. In still another embodiment, the insulating material separating the trace from the reference plane is modified to change its dielectric constant and/or magnetic permeability in the vicinity of the selected trace.

BEST AVAILABLE COPY

WO 01/01453 A2